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MARK BECK	(Depositor's name)
Mark Beck	(Signature)
2/10/04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/626,089	07/24/2003	Chun Jiang	L01-029D1	4530

TITLE OF INVENTION: ZERO POWER MEMORY CELL WITH REDUCED THRESHOLD VOLTAGE

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	04/21/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRAN, MAI HUONG C	2818	257-315000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.☒ "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. Inclusion of assignee data is only appropriate when an assignment has been previously submitted to the USPTO or is being submitted under separate cover. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Lattice Semiconductor Corporation

(B) RESIDENCE: (CITY AND STATE OR COUNTRY)

Hillsboro, Oregon

Please check the appropriate assignee category or categories (will not be printed on the patent):

☐ individual ☒ corporation or other private group entity ☐ government

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☒ Issue Fee☐ Publication Fee☐ Advance Order - # of Copies _____

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2/10/04

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